

Attorney Docket # 5367-220PUS

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Patent

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re National Phase PCT Application of

Volker HÄRLE

Int'l PCT Serial No.: PCT/DE2004/001593

Int'l Filing Date:: July 22, 2004

For: Method for the Production of a Plurality of  
Opto-Electronic Semiconductor Chips and  
Opto-Electronic Semiconductor Chip

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop **PCT**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

S I R:

In compliance with the duty of disclosure under 37 C.F.R. § 1.56 and in accordance with the practice under 37 C.F.R. §§ 1.97 and 1.98, the Examiner's attention is directed to the documents listed on the enclosed Form PTO SB/08A.

In accordance with 37 C.F.R §§1.97(g) and (h), the filing of this Information Disclosure Statement should not be construed as a representation that a search has been made or that information cited is, or is considered to be, material to patentability as defined in §1.56(b), or that any cited document listed or attached is (or constitutes) prior art. Unless otherwise indicated, the date of publication indicated for an item is taken from the face of the item and Applicant(s) reserve(s) the right to prove that the date of publication is in fact different.

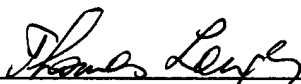
Submitted herewith is a copy of the International Search Report issued in connection with International Application on which the present U.S. National Phase application is based.

Copies of the references cited in the International Search Report have been supplied by WIPO to the USPTO. Therefore, copies of the cited references in the International Search Report are not being provided herewith in accordance with current U.S. practice because they should be on file already at the USPTO.

It is respectfully requested that the Examiner return an initialed copy of the attached Form PTO SB/08A to confirm that the publications supplied by WIPO and listed therein have been considered and made of record.

Respectfully submitted,  
COHEN, PONTANI, LIEBERMAN & PAVANE

By



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Dated: January 30, 2006

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Sheet 1 of 2

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Volker HÄRLE

5367-220PUS

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Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>6</sup>
		Country Code <sup>3</sup> : Number <sup>4</sup> : Kind-Code <sup>5</sup> (if known)				
		EP 0 472 221 A2	02/26/1992	NEC Corporation		
		EP 1 005 067 A2	05/31/2000	Sony Corporation		
		EP 0 388 733 A1	09/26/1990	Fujitsu Limited		
		JP 05 226781 A	12/13/1993	Fujitsu Ltd.		

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	Application Number	
	Filing Date	
	First Named Inventor	Volker HARLE
	Art Unit	
Examiner Name		
Sheet 2 of 2	Attorney Docket Number	5367-220PUS

<b>NON PATENT LITERATURE DOCUMENTS</b>			
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
		Yang et al., "Selective area deposited blue GaN-InGaN multiple-quantum well light emitting diodes over silicon substrate", Applied Physics Letters, American Institute of Physics, New York, Vol. 76, No. 3, pp. 273-275, January 17, 2000	
		Miyata et al., "Selective growth of nanocrystalline Si dots using an ultrathin-Si-oxide/oxynitride mask", Applied Physics Letters, American Institute of Physics, New York, Vol. 77, No. 11, pp. 1620-1622, September 11, 2000	

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